Comparison between Two Different Snubbers of a Boost Converter for Achieving Soft-switching Features

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This paper presents a comparison between two different snubbers of a boost converter for achieving soft-switching features. To obtain high current capability and high conversion efficiency, a boost converter is operated in a hard-switching mode, which will induce high component stresses, high switching losses of active switches, and serious electromagnetic interference (EMI). To overcome these disadvantages, two different snubbers using simple passive components that are incorporated in boost converters are compared. The two different snubbers are a buck snubber and a boost snubber. In this study, the performance and efficiency of boost converters with the buck snubber and with the boost snubber are obtained. From the characteristic analyses and experimental results, it has been verified that the boost converter with the boost snubber achieved lower component stresses, lower switching losses of active switches and higher conversion efficiency than the boost converter with the buck snubber.

1. Introduction

Green energies include photovoltaic (PV), wind, and geothermal energy, which have the features of cleanliness, freedom from maintenance, and abundance. Currently, PV panels are one of the most widely applied green energies. To maximize the power of PV panels, the maximum power point tracking method is incorporated in a microchip. However, the output power of PV panels is unstable and has a low output voltage. Therefore, PV panels require a boost converter to stabilize the output power and boost voltage. A boost converter is operated in a hard-switching mode, which induces serious stresses and switching losses of active switches. These disadvantages lead to low transfer efficiency and serious electromagnetic interference (EMI) of the boost converter.

To overcome these disadvantages, a converter with an inductor-capacitor-diode (LCD) snubber that was first reported in 2009 is shown in Fig. 1. Although the boost converter with the LCD snubber can decrease component stresses and switching losses of active switches, it still has high EMI and low transfer efficiency. In previous studies, a boost converter with a
buck snubber or a boost snubber was proposed to alleviate the drawbacks of the boost converter with an LCD snubber, as shown in Figs. 2 and 3. The structures of the boost converters with the buck snubber and with the boost snubber are analyzed in Sect. 2. The design considerations, experimental results, and efficiency of the boost converters with the buck snubber and with the boost snubber are compared in Sects. 3 and 4. Finally, a conclusion is given in Sect. 5.
2. Structure Analyses of Boost Converters with a Buck Snubber and with a Boost Snubber

Figure 2 shows the structure of the boost converter with a buck snubber for achieving soft-switching functions. The buck snubber consists of an active switch ($M_b$), a resonant capacitor ($C_1$), a resonant inductor ($L_1$), and three diodes ($D_1$, $D_2$, and $D_3$). The operational methods of the boost converter with a buck snubber can be separated into four modes, concisely explained as follows:

**[Mode 1]**
When the main switch ($M_a$) and the auxiliary switch ($M_b$) are turned on at the same interval, the main inductor ($L_a$) is charged and the main current $i_{L_a}$ flowing through the path $V_{DC} \rightarrow L_a \rightarrow M_a$ is linearly increased. Simultaneously, the auxiliary inductor ($L_1$) is also charged by the capacitor ($C_1$). The current $i_{L_1}$ of the auxiliary inductor ($L_1$) flowing through the path $C_1 \rightarrow D_2 \rightarrow M_b \rightarrow L_1 \rightarrow R$ is linearly increased. In this interval, the main switch ($M_a$) is turned on under a hard-switching condition. Therefore, the main switch ($M_a$) induces serious switching losses. The equivalent circuit is shown in Fig. 4(a).

**[Mode 2]**
In this interval, when the voltage $V_{c_1}$ of the capacitor ($C_1$) is dropped to zero, the auxiliary switch ($M_b$) is turned off under a zero-voltage transient (ZVT) condition to implement a soft-
switching feature. At this time, the main switch \( (M_a) \) is continuously turned on and the auxiliary current \( i_{L1} \) flows through the path of \( D_3 \rightarrow L_1 \rightarrow R \). The equivalent circuit is shown in Fig. 4(b).

[Mode 3]

In this interval, the auxiliary switch \( (M_b) \) is kept turned off. At this time, the main switch \( (M_a) \) is turned off under a ZVT condition to implement a soft-switching feature. The current \( i_{La} \) of the main inductor \( (L_a) \) flows through the path \( V_{DC} \rightarrow L_a \rightarrow D_1 \rightarrow C_1 \), and the current \( i_{L1} \) of the auxiliary inductor \( (L_1) \) continuously flows through the path \( D_3 \rightarrow L_1 \rightarrow R \). The equivalent circuit is shown in Fig. 4(c).

[Mode 4]

In this interval, when the voltage of the capacitor \( (C_1) \) is charged to \( V_{c1} \), the diode \( (D_1) \) is turned off. The current \( i_{La} \) of the main inductor \( (L_a) \) flowing through the path \( V_{DC} \rightarrow L_a \rightarrow D_a \rightarrow R \) is linearly decreased. The operation of the boost converter with a buck snubber over one switching cycle is completed, and the equivalent circuit is shown in Fig. 4(d).

Figure 3 shows the structure of the boost converter with a boost snubber for achieving soft-switching functions. The boost snubber consists of an active switch \( (M_b) \), a resonant capacitor \( (C_1) \), a resonant inductor \( (L_1) \), and three diodes \( (D_1, D_2, \text{and} D_3) \). The operational modes of the boost converter with a buck snubber can be separated into eight modes, concisely explained as follows:

[Mode 1]

When the main switch \( (M_a) \) is turned on and the auxiliary switch \( (M_b) \) is turned off in the same interval, the main inductor \( (L_a) \) is charged and the main current \( i_{La} \) flowing through the path \( V_s \rightarrow L_a \rightarrow M_a \) is linearly increased. Simultaneously, the capacitor \( (C_o) \) delivers power to load \( (R) \). The equivalent circuit is shown in Fig. 5(a).

[Mode 2]

In this interval, the main switch \( (M_a) \) is turned off, and the auxiliary switch \( (M_b) \) is kept off. The diode \( (D_a) \) is turned on and the current \( i_{La} \) of the main inductor \( (L_a) \) flowing through the path \( V_s \rightarrow L_a \rightarrow D_a \rightarrow R \) is linearly decreased. The equivalent circuit is shown in Fig. 5(b).

[Mode 3]

In this interval, the main switch \( (M_a) \) is kept off, and the auxiliary switch \( (M_b) \) is turned on. The current \( i_{La} \) of the main inductor \( (L_a) \) is divided into two current paths \( i_{L1} \) and \( i_{D1} \), namely, \( V_s \rightarrow L_a \rightarrow L_1 \rightarrow M_b \) and \( V_s \rightarrow L_a \rightarrow D_a \rightarrow R \). The equivalent circuit is shown in Fig. 5(c).

[Mode 4]

In this interval, when the main current \( i_{La} \) is equal to the auxiliary current \( i_{L1} \), the diode \( (D_a) \) is turned off. Simultaneously, the capacitor \( (C_o) \) delivers power to load \( (R) \). The equivalent circuit is shown in Fig. 5(d).

[Mode 5]

In this interval, when the main current \( i_{La} \) is less than the auxiliary current \( i_{L1} \), the body diode of the main switch \( (M_a) \) is turned on, creating a ZVS condition for the main switch \( (M_a) \). Simultaneously, the capacitor \( (C_o) \) continuously delivers power to load \( (R) \). The equivalent circuit is shown in Fig. 5(e).
Fig. 5. (Color online) Equivalent circuit modes of boost converter with a boost snubber. (a) Mode 1, (b) Mode 2, (c) Mode 3, (d) Mode 4, (e) Mode 5, (f) Mode 6, (g) Mode 7, and (h) Mode 8.
[Mode 6]
In this interval, the main switch ($M_a$) is turned on and the auxiliary switch ($M_b$) is kept on.
The current $i_{La}$ of the main inductor ($L_a$) is divided into two current paths $i_{L1}$ and $i_{Ma}$, namely, $V_s \rightarrow L_a \rightarrow M_a$ and $V_s \rightarrow L_a \rightarrow L_1 \rightarrow M_b$. Simultaneously, the capacitor ($C_o$) delivers power to load ($R$). The equivalent circuit is shown in Fig. 5(f).

[Mode 7]
In this interval, the main switch ($M_a$) is kept on and the auxiliary switch ($M_b$) is turned off.
The current $i_{L1}$ of the auxiliary inductor ($L_1$) is transferred to the capacitor ($C_1$), and the diode ($D_1$) is turned on. The current $i_{L1}$ of the auxiliary inductor ($L_1$) flows through the path $L_1 \rightarrow D_1 \rightarrow C_1$. Simultaneously, the capacitor ($C_o$) continuously delivers power to load ($R$). The equivalent circuit is shown in Fig. 5(g).

[Mode 8]
In this interval, when the voltage $V_{c1}$ of the capacitor ($C_1$) is dropped to zero, the diodes ($D_2$ and $D_a$) are turned on. The current $i_{La}$ of the main inductor ($L_a$) is divided into three current paths $i_{Ma}$, $i_{L1}$, and $i_{Da}$, namely, $V_s \rightarrow L_a \rightarrow M_a$, $V_s \rightarrow L_a \rightarrow L_1 \rightarrow D_1 \rightarrow D_2 \rightarrow R$, and $V_s \rightarrow L_a \rightarrow D_a \rightarrow R$. The operation of the boost converter with a boost snubber over one switching cycle is completed. The equivalent circuit is shown in Fig. 5(h).

3. Design Considerations of Boost Converters with a Buck Snubber and with a Boost Snubber

This section presents the design considerations of the boost converters with a buck snubber and with a boost snubber. The design considerations are divided into two parts: the first part is the boost converter with a buck snubber and the second part is the boost converter with a boost snubber. The design considerations of their key components are depicted as follows:

A) Selection of Main Switch ($M_a$) and Auxiliary Switch ($M_b$)

In Figs. 2 and 3, the main switch ($M_a$) and auxiliary switch ($M_b$) use metal-oxide-semiconductor field-effect transistors (MOSFETs). The consideration of the MOSFETs involves their conduction losses and voltage stresses. Therefore, MOSFETs with low channel resistance $R_{ds(on)}$ are adopted because they can usually keep conduction losses low. In this application, the IRFP450 MOSFETs with a drain-source breakdown voltage of 500 V and a channel resistance $R_{ds(on)}$ of 0.4 Ω are selected for use.

B) Selection of Main Inductor ($L_a$)

In Fig. 2, the operated current of the main inductor ($L_a$) of the boost converter with a buck snubber is designed with a continuous-conduction mode (CCM), in which the design condition is

$$L_a = \frac{V_{out} T_s}{2 I_{Ob} D (1 - D)^2},$$

where $V_{out}$ is the output voltage of the boost converter with a buck snubber, $T_s$ is the switching period of the main switch ($M_a$), $I_{Ob}$ is the boundary output current of the main inductor ($L_a$), and $D$ is the duty ratio of the main switch ($M_a$).
In Fig. 3, the operated current of the main inductor \(L_a\) of the boost converter with a boost snubber is designed with a CCM, in which the design condition is

\[
L_a = \frac{V_{out}^2 T_s}{2 P_{out}} \left(1 - \frac{V_{out}}{V_s}\right),
\]

where \(V_s\) is the voltage and \(P_{out}\) is the output power of the boost converter with a boost snubber.

**C) Selection of Output Filter Capacitor \((C_o)\)**

In Figs. 2 and 3, the output filter capacitor \((C_o)\) is selected to buffer the output voltages, suppress spikes, and filter ripples of the boost converter. Three 470 µF electrolytic capacitors in parallel are selected.

4. **Experimental Results**

To compare the performances of the boost converter with a buck snubber and with a boost snubber, two prototypes are built with the following specifications:

- input voltage: \(V_s = 156\) V\(_{DC}\),
- output voltage: \(V_o = 400\) V\(_{DC}\),
- output current: \(I_o = 1\) A,
- output power: \(P_{out} = 400\) W, and
- switching frequency of active switches: \(f = 50\) kHz.

The experimental results of the boost converter with an LCD snubber, the boost converter with a buck snubber, and the boost converter with a boost snubber are described in this section. Figure 6 shows the voltage waveforms of the main switch \((M_o)\) of the boost converter with an LCD snubber. It can be seen that the main switch \((M_o)\) has a hard-switching feature and high-peak-current stresses at the turn-on transient and a ZVT switching feature and high-peak-voltage stresses at the turn-off transient. Figure 7 shows the voltage waveforms of the main switch \((M_o)\).
of the boost converter with a buck snubber. It can be seen that the main switch ($M_a$) has a hard-switching feature at the turn-on transient and a ZVT switching feature at the turn-off transient. Figure 8 shows the voltage waveforms of the main switch ($M_a$) of the boost converter with a boost snubber. It can be seen that the main switch ($M_a$) has a ZVS feature at the turn-on transient and a hard-switching feature at the turn-off transient. Figure 9 shows the efficiency measurements of the boost converters with the three different snubbers, from which it can be seen that the boost converter with a boost snubber obtains the highest efficiency. This is because the main switch ($M_a$) of the boost converters with an LCD snubber and with a buck snubber is operated with the same hard switching at the turn-on transient, resulting in high switching losses and high peak currents. The performances of the boost converters with the three different snubbers are summarized in Table 1.
5. Conclusions

In this paper, two different snubbers of a boost converter for achieving soft-switching features were compared. The performances of a traditional LCD snubber, a buck snubber, and a snubber incorporated in boost converters were compared. From the measured voltage and current waveforms of the boost converters with the LCD snubber, with the buck snubber and with the boost snubber, it can be seen that the boost converter with the boost snubber had the highest efficiency. This is because the main switch ($M_a$) of the boost converter with the boost snubber had a ZVS feature operated at the turn-on transient. From the experimental results, it was confirmed that the efficiency of the boost converter with the boost snubber is greater than those of the boost converters with the other two snubbers.

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Author Contributions

All authors contributed to this paper. Cheng-Tao Tsai wrote the paper, Yan-Wei Huang contributed to the experimental results of the proposed circuits, and Tsair-Chun Liang proofread the paper.

Conflicts of Interest

The authors declare that there are no conflicts of interest regarding the publication of this paper.

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