

Thermoelectric Properties of Polysilicon Films Doped with Phosphorus and Boron

F. Völklein and H. Baltes

Physical Electronics Laboratory Institute of Quantum Electronics
Swiss Federal Institute of Technology ETH Hoenggerberg,
HPT CH-8093 Zurich, Switzerland

(Received January 17, 1992; accepted March 27, 1992)

Key words: polysilicon film, thermoelectric properties, thermoelectric sensor

The thermoelectric properties of polysilicon films doped with boron or phosphorus are investigated within the temperature range of 80-400 K. The results are analyzed in the framework of the grain boundary trapping model and semiconductor transport theory. Correlations between electrical conductivity and Seebeck coefficient data are discussed.

1. Introduction

Polysilicon films are important constituents of the current integrated circuit (IC) technology.⁽¹⁻³⁾ Therefore, their structural and electrical properties have been investigated and reported by various researchers.⁽⁴⁻¹²⁾ In view of the rather poor thermoelectric efficiency of polysilicon, the thrust of these investigations was not focused on the thermoelectric properties. On the other hand, new integrated sensors have been developed recently within the framework of standard IC technologies. A variety of thermoelectric sensors integrated on bulk silicon wafers, membranes, or beams have been realized,^(13,14) which demonstrate the well-known advantages of on-chip signal conditioning and temperature compensation. New IC-compatible pressure sensors are based on polysilicon films.⁽¹⁵⁻²¹⁾ Recently, thermoelectric thin-film sensors were developed using standard CMOS processes,⁽²²⁻²⁴⁾ where polysilicon (normally used as a gate electrode) serves as one thermoelectric leg of the thermoelements or thermopiles.

For design, modelling, and optimization of such sensors, the knowledge of the